

ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention

Integrated Circuit Chip Having A Ringed Wiring Layer Interposed
Between A Contact Layer And A Wiring Grid

Submission Type : Utility Patent Filing

Application Number:

10/604995



EFS ID:

46816

Server Response:

Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
ICON1	1994
WGEN24	Filename= all XMLs having <invention-title> BusinessRule= Common elements across multiple XML documents within the submission package do not have synchronized data content. #Supporting Msg:<invention-title> not the same or having font format.

First Named Applicant: Thomas Bednar

Attorney Docket Number: BUR920020107US1

Timestamp: 2003-08-29 13:35:54 EDT

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us-assignment	BUR920020107US1assign3.tif	15428	2003-08-29
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us-declaration	BUR920020107US1dec3.tif	23316	2003-08-29
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